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Digital Systems Design Using VHDL

Digital Systems Design Using VHDL

Second Edition

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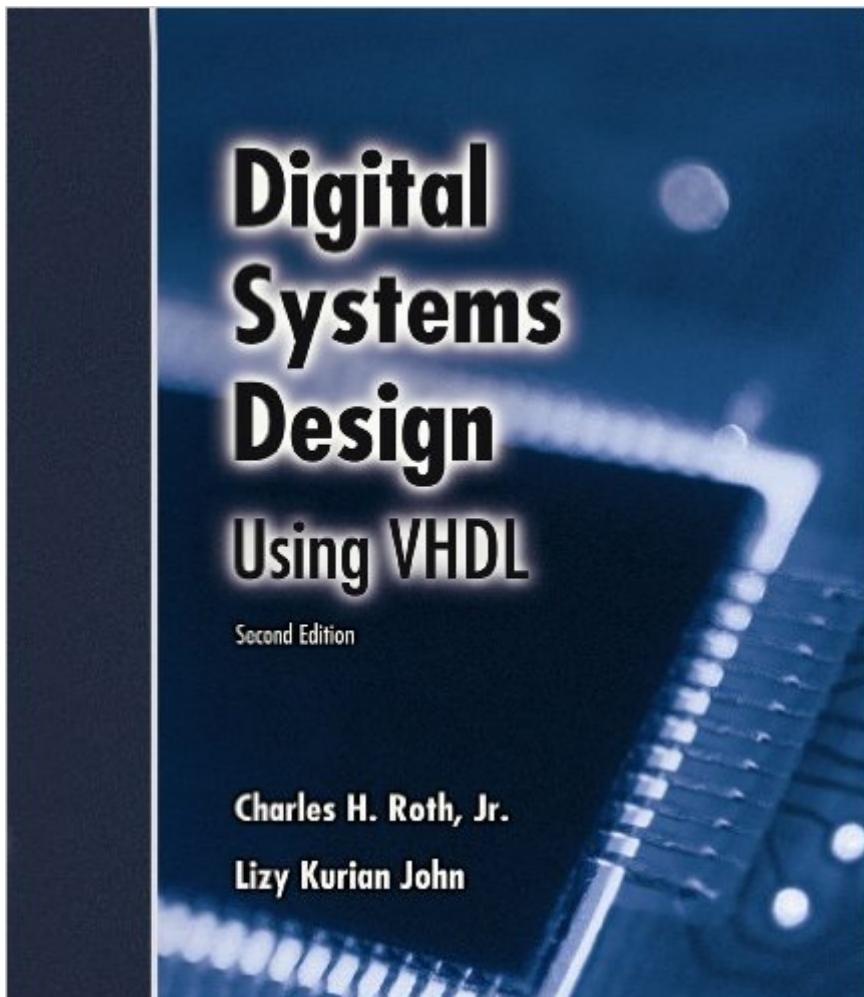
Synopsis

Written for an advanced-level course in digital systems design, DIGITAL SYSTEMS DESIGN USING VHDL integrates the use of the industry-standard hardware description language VHDL into the digital design process. Following a review of basic concepts of logic design, the author introduces the basics of VHDL, and then incorporates more coverage of advanced VHDL topics. Rather than simply teach VHDL as a programming language, this book emphasizes the practical use of VHDL in the digital design process.Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

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Customer Reviews

After an absence of not doing logic design for 20 years, I want to pick up the trade again. I selected this book. It turns out to be an EXCELLENT choice. In one chapter, the author reviews everything I learned 20 years ago on combinational and sequential logic. He then introduces VHDL to model both kinds of logic. From then on logic design is done with VHDL and implemented with programmable gate arrays. I would recommend this book to people who have studied a beginning

logic design book such as Mano's.

I bought this book and it was recommended as a textbook for the VHDL course. I have this book and I am 100% sure that this book is NOT one of the good VHDL books you need/want in your library. It belongs to the "OK" level.chapter one:is the most valuable chapter. it contains an excellent review for every thing in digital logic, and guess what?chapter two:is a poor organized chapter that presents VHDL basics. after reading this chapter you will stop reading because you will start searching for other well organized books! that was my situation and 9 of my friends got similar situation.after reading 3 books in VHDL, I highly recommend:1- only the first chapter of this book2- Circuit Design with VHDL by Pedroni3- some examples from "Design Recipes for FPGAs" by Wilson

Great book for learning VHDL and to use for reference. Will definitely keep in my library. I don't think this is the best material for advanced users as some of my assignments require me to look elsewhere.

This was a book required by a VHDL class I took while completing my undergrad degree. I only ever opened it to look at the homework questions. I never had to read it because I had a good professor, but when skimming through the chapters, it didn't seem like any of the ideas/concepts were presented in a way as clear as the professor made them.

A lot of "examples" in this text need to be updated, as the coding in more current IDEs don't use that specific terminology anymore. As for general understanding, it's alright, clear and lots of examples, but as before, if you're using a more current IDE software (like Xilinx or similar) a lot of the coding is incorrect and doesn't translate to the program, but the structure of the coding does. Just be careful if you're trying to implement what you're learning on your own.

This book was required for a class and ended up being a great resource for learning VHDL. I only rented it because of the price and was disappointed to have to send it back.

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